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M+W Zander - Glossary

... **interconnect** the devices on a wafer and to **interconnect** external devices ... **BGA**, see **ball grid array**. ... that shows the relative location of product, **test**, and dummy ...
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BGA : see **ball grid array**. **BHT** : see Brinell hardness **test**. ... **bridging fault**
n : a fault modeled as a short-circuit between two nets on a die. ...
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